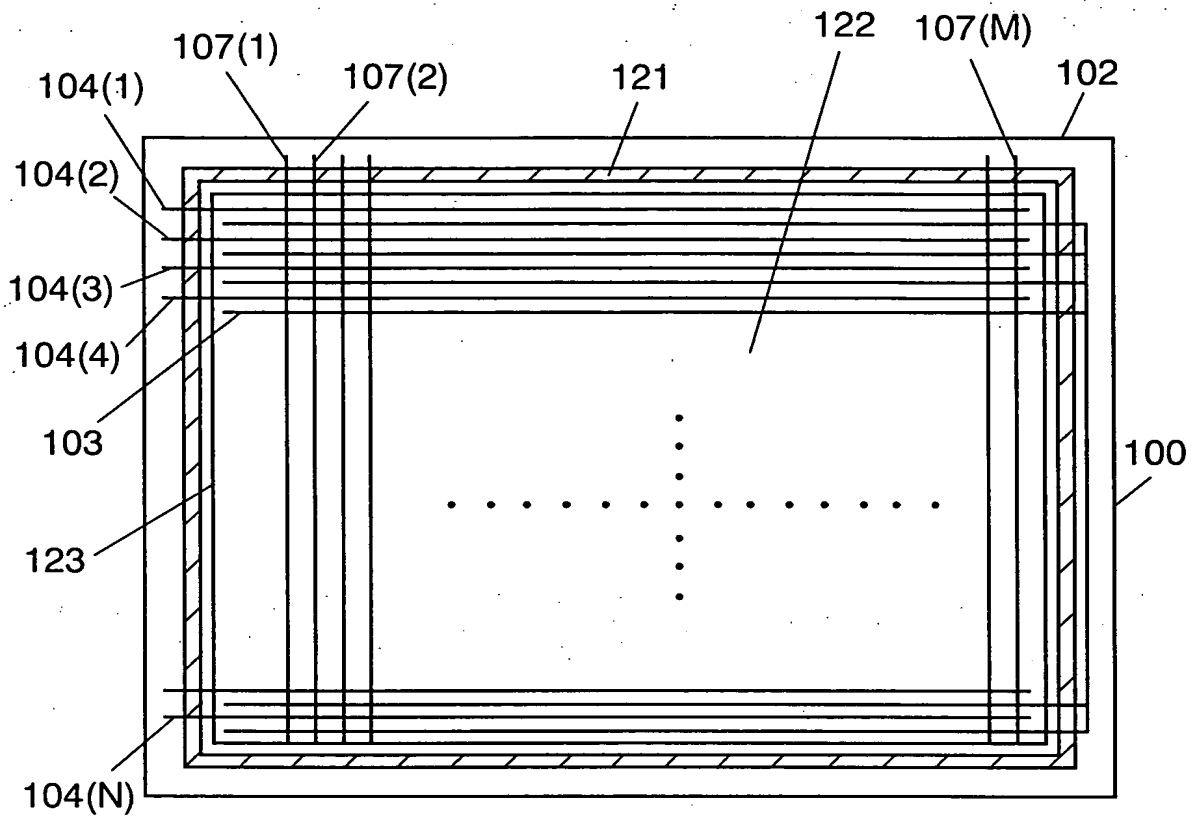


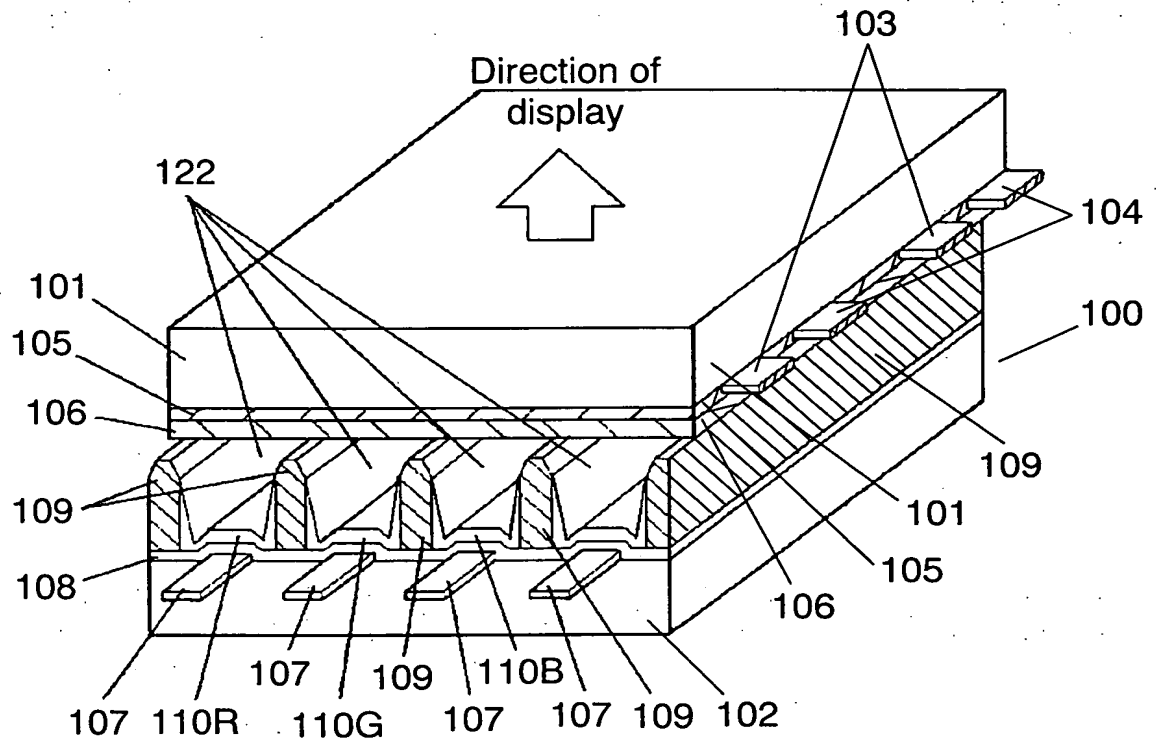
1/5

FIG. 1



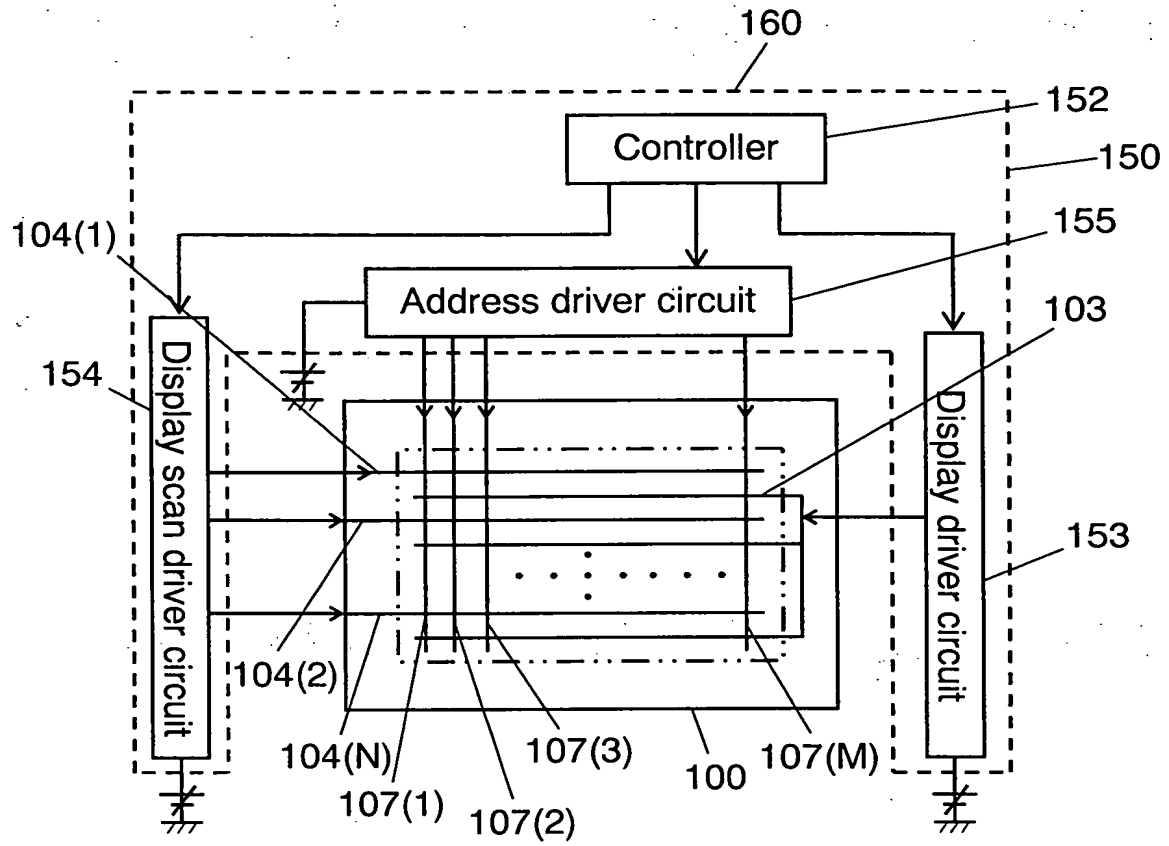
2/5

FIG. 2



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FIG. 3



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FIG. 4

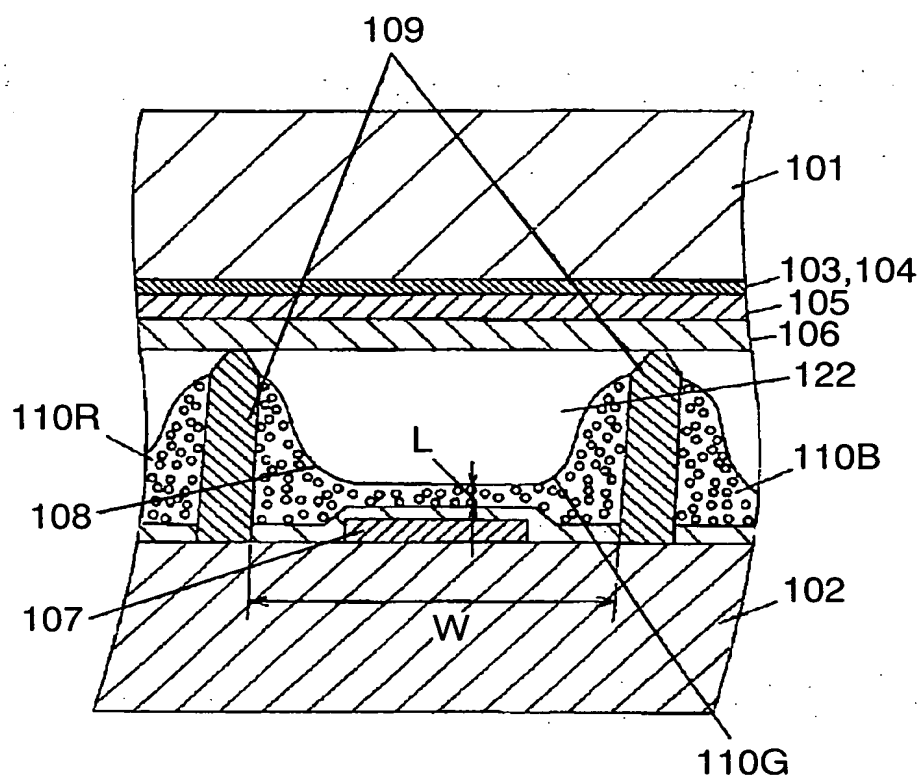
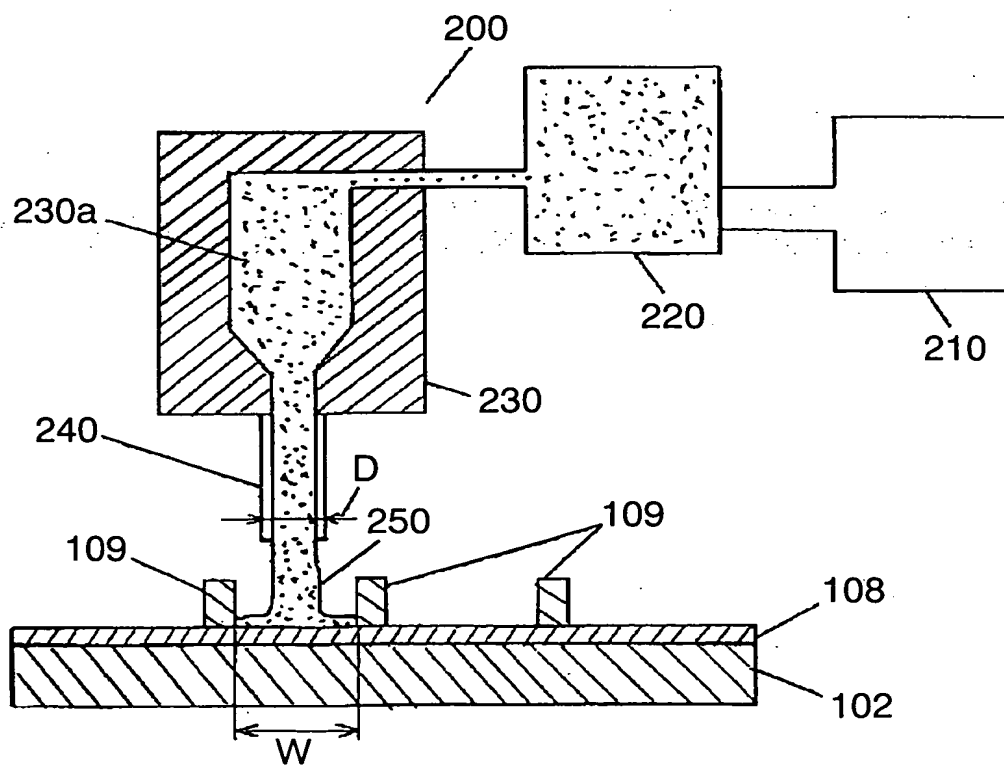


FIG. 5



Reference marks in the drawings

- 1 0 0 P D P
- 1 0 1 Front glass substrate
- 1 0 2 Rear glass substrate
- 1 0 3 Display electrode
- 1 0 4 Display scan slectrode
- 1 0 5, 1 0 8 Dielectric glass layer
- 1 0 6 Mg O protective layer
- 1 0 7 Address electrode
- 1 0 9 Barrier rib
- 1 1 0 R Phosphor layer (red)
- 1 1 0 G Phosphor layer (green)
- 1 1 0 B Phosphor layer (blue)
- 1 2 1 Hermetic seal layer
- 1 2 2 Discharge apace
- 1 2 3 Image display area
- 1 5 0 Driver
- 1 5 2 Controller
- 1 5 3 Display driver circuit
- 1 5 4 Display scan circuit